

b. Using DIT-FFT algorithm, compute the DFT of a sequence x(n) = (1, 1, 1, 1, 0, 0, 0, 0).

(10 Marks)

Module-3

- Let the coefficients a three stage FIR lattice structure be $K_1 = 0.1$, $K_2 = 0.2$ and $K_3 = 0.3$. 5 a. Find the coefficients of the direct form – I FIR filter and draw its block diagram. (10 Marks)
 - A linear time-invariant system is described by the following input-output relation. b.
 - 2y(n) y(n-2) 4y(n-3) = 3x(n-2). Realize the system in the following forms:
 - Direct form I realization. i)
 - Direct form II realization. ii)

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(10 Marks)

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(10 Marks)

(12 Marks)

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OR

6 a. The desired frequency response of a lowpass filter is given by

$$H_{d}(e^{jw}) = H_{d}(w) = \begin{cases} e^{-j3w}, & |w| < \frac{3\pi}{4} \\ 0, & \frac{3\pi}{4} < |w| < \pi \end{cases}$$

Determine the frequency response of the FIR filter it Hamming window is used with N = 7. (10 Marks) Find the lattice-ladder structure for the filter given by the following difference equation:

b. Find the lattice-ladder structure for the filter given by the following difference equation:

$$y(n) + \frac{3}{4}(n-1) + \frac{1}{4}y(n-2) = x(n) + 2x(n-1).$$
(10 Marks)

Module-4

7 a. Obtain a parallel realization for the system for the system described by

$$H(Z) = \frac{(1+z^{-1})(1+2z^{-1})}{\left(1+\frac{1}{2}z^{-1}\right)\left(1-\frac{1}{4}z^{-1}\right)\left(1+\frac{1}{8}z^{-1}\right)}$$

b. Obtain the cascade realization of system $H(z) = [2z^{-1} - z^{-2}] \cdot [z^{-1} - z^{-2}].$ (10 Marks)

OR

- 8 a. Design a Butterworth analog high pass filter that will meet the following specifications:
 - i) Maximum passband attenuation = 2dB
 - ii) Passband edge frequency = 200rad/sec
 - iii) Minimum stopband attenuation = 20dB
 - iv) Stopband edge frequency = 100rad/sec.

b. Realize the FIR filter whose transfer function is given by $H(z) = 1 + \frac{3}{4}z^{-1} + \frac{17}{8}z^{-2} + \frac{3}{4}z^{-3} + z^{-4} \text{ using direct form} - I.$ (08 Marks)

Module-5

9 a. Explain the digital signal processors based on the Harvard architecture.(10 Marks)b. Find the signed Q-15 representation for the decimal number 0.560123.(10 Marks)

OR

a. Explain with neat block diagram floating point DS processor (TMS320C3X). (10 Marks)
b. Explain fixed-point digital signal processors (architecture of the TMS320C54X family).

(10 Marks)